

DR GUNJAN THAKUR (PhD)

Teaching Experience 7 Years

Research Experience 3 years

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Ahmedabad (382421), Gujarat, India

- Highly motivated and dedicated professional with over 10 years of teaching experience in Engineering College
- Hands-on experience in teaching, assisting students for project work, practical assessment, leading industrial visits and handling routine administrative tasks
- Profound knowledge of the subject areas and ability to teach students by using various innovative and practical teaching techniques with excellent communication and written skills
- Received student feedback of more than 90% in FPI (Faculty Performance Index)

WORK EXPERIENCE

Digital Design Engineer (Student's Mentor) | Neurapses Private Limited 2017 – Present

- Supervising and guiding trainees in developing various modules for Robotic Design
- Conducting various technical training in the organization to enhance competency of the trainees

Research Work | Reva Institute of Technology & Management (RITM), Bangalore 2015 – 2017

- Design and implemented various Digital Circuits for System on Chip (SoC) device in Nanotechnology
- Worked on different types of technology to realize Digital Circuits using Hardware Description Language (HDL)

Assistant Professor | Department of Information Science, Vemana Institute of Technology, Bangalore 2007 – 2014

- Teaching subjects such as Digital Electronics, Microprocessors and Microcontrollers, Computer Network, Electronic Circuits, Information & Network Security, Basics of Computer, Wireless Communication
- Organized various Seminars and workshops for the Department
- Mentored various students during their project work
- Actively participated in administrative work and assignments

EDUCATION

- Doctor of Philosophy (PhD) in Electronics and Communication from Visvesvaraya Technological University (VTU), Belgaum, India (2012-2019)
- Master of Technology (M.Tech.) in VLSI Design from Nirma University, Ahmedabad, India (2005-2007)
- Bachelor of Engineering (B.E) in Electronics and Communication from Bhavnagar University, Gujarat, India (2000-2004)
- Diploma in Electronics and Communication from Govt. Polytechnic for Girls, Ahmedabad, India (1998-2000)

SUBJECTS HANDLED

Digital Electronics, Microprocessors and Microcontrollers, Computer Network, Electronic Circuits, Information & Network Security, Basics of Computer, Wireless Communication

LANGUAGE & SKILL SET

Verilog (Hardware Description Language) | Curriculum Development | Training & Mentoring | Assessment & Evaluation | Effective Communication Skills | Student Counseling | Student Management Time Management | Learning Methodologies

PUBLICATIONS

- “Design and Implementation of Crossbar Scheduler for System On-Chip (SoC) Network in Quantum dot Cellular Automata Technology” **Internet Technology Letters, IITL, 2018, John Wiley and Sons, Wiley Publications.**
- “A Comprehensive Digital Cross Connect (DCS) for Buffered and Unbuffered Switching Applications” **International Journal of Control Theory and Applications, IJCTA, February 2016, Scopus**
- “An Area Efficient Multiplexer for Crossbar Arbiter using Quantum Dot Cellular Automata”, IEEE Conference, SVCE 2016, **IEEE Xplore (Digital Library).**
- “A Digital Cross Connect (DCS) Switch For Multicast and Broadcast Traffic” IEEE Conference, INDICON 2016, **IEEE Xplore (Digital Library).**
- “Wireless Digital Cross Connect SoC for Optical Networks using FPGA”, IEEE Conference, INDIACOM 2016, **IEEE Xplore (Digital Library).**
- “Digital Cross Connect Systems DCS – a Technology Survey, Key Challenges, Architecture and Applications” Fourth International Conference on Advances in Signal Processing and Communication, SPC 2015, CROSSREF INDEXED.
- “A Comprehensive Survey of Digital Switching Methods & Techniques” The Fifth International Joint Conference on Advances in Engineering and Technology, AET 2014, CROSSREF INDEXED, RPS ONLINE.
- “Modelling of Fiber Bragg Grating for sensor application” International Conference on Optical Engineering (ICOE) – 2012, **IEEE Xplore (Digital Library).**